Single Open Loop, High-Speed Buffer



50LD

Description

This High-Speed Voltage Buffer increases the available current for a low-impedance load that is being driven by a high-impedance source. It does so while maintaining a 1:1 voltage gain through the system. The buffer has a 20 kΩ input impedance and a 50 Ω output impedance.

Specifications

50LD Specifications	
Performance	
Source Impedance	20 kΩ
Output Impedance	50 Ω
Maximum Input Voltage	± 5 V
Maximum Output Current	300 mA
Slew Rate[a]	200 V/µs
Voltage Gain	1:1
Bandwidth	20 MHz
General	
Input/Output Connectors	Female SMA
Device Input Power	USB (5 V, 500 mA)
Operating Temperature	5 to 35 °C

a. Slew rate is defined at 10% and 90% cutoffs.



Mechanical Drawings

